Instructions: Time 10 minutes. Open book and notes exam. No electronics. Please answer all problems in the space provided and limit your answer to the space provided. No questions are allowed.

P1. Draw a two-way associative cache with the following specifications: size $=32 \mathrm{~KB}$, block size $=64$ bytes, word size $=4$ bytes, address width $=32$ bits, and write through scheme. Specify the sizes (in bits) of the block offset, index, and tag.

## The solution is:

$$
\mathrm{m}=32
$$

$$
\mathrm{n} \quad=\lg _{2}(\text { block size in bits })=\lg _{2}(64 \times 8 \text { bits })=9 \text { bits }
$$

$$
\text { <block offset> } \quad=\lg _{2}(\text { block size in bytes })=\lg _{2}(64)=6 \text { bits }
$$

$$
\text { Number of blocks }=32 \mathrm{~KB} / 64 \text { bytes }=512 \text { blocks }
$$

$$
\text { Number of sets } \quad=512 / 2=256 \text { sets }
$$

$$
\mathbf{k}=<\text { index }>\quad=\lg _{2}(\text { No. of sets })=\lg _{2}(256)=8 \text { bits }
$$

$$
<\operatorname{tag}>\quad=32-<\text { inde } \gg-<\text { block offset }>=32-8-6=18 \text { bits }
$$



