0907335 Computer Organization (Fall 2014) <u>Quiz 1</u>			
سلي:	الرقم التسل	رقم التسجيل:	الاسم:
<u>Instructions</u> : Time 20 minutes. Open book and notes exam. No electronics. Please answer all problems in the space provided and limit your answer to the space provided. No questions are allowed. <i>Good Luck</i>			
Q1. At what clock seconds? Assume	rate a processo that this process	or runs if it executes a program consists sor takes an average two cycles to execu	sting of $10^9$ instructions in 4.0 te each instruction.
Clock Rate = Instruction Count × CPI / CPU Time			
$= 10^9 \times 2 / 4$ = 0.5 GHz	4.0		
Q2. For the following C statement, what is the corresponding MIPS assembly code? Assume that all variables are one-word signed integers. Also assume that the compiler maps the starting addresses of Arrays A and B to Registers \$s0 and \$s1, respectively.			
A[0]	= B[3] + 7	7;	
lw addi sw	\$t0, 12(\$ \$t1, \$t0, \$t1, 0(\$s(	s1) 7 0)	

```
Q3. Translate the following MIPS instruction to machine code. First, specify the fields of the instruction
  word then convert the word to a 32-bit binary number. Note that the opcode of beg instruction is 4.
                                                                                  <2 points>
            beq $s0, $s1, -5
     beq : rs: rt : const
    4:16:17:-5
   (000100:10000:10001:111111111111111)_{2}
Q4. Convert the following C function to MIPS assembly code.
                                                                                  <3 points>
      int Max(int x, int y) {
            if (x \ge y)
                   return x;
            else
                   return y;
      }
            $t0, $a0, $a1
Max:
       slt
       bne
             $t0, $zero, else
             $v0, $a0, $zero
       add
              $ra
       jr
else: add
              $v0, $a1, $zero
       jr
              $ra
```