

Princess Sumaya University of Technology
Computer Engineering Department
22446: Microprocessors
Spring 2011

Instructor	Dr. Gheith Abandah
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Homepage	http://www.abandah.com/gheith
Office	EE 448
Office Hours	Sun 11:00 -12:00, Mon 11:30-12:30, and Thu 9:00-10:00
Prerequisites	22345: Assembly Language
Time and Room	Section 1: Sun, Tue, and Thu 10:00-11:00 in Room EE 342 Section 2: Mon and Wed 12:30-2:00 in Room EE 342
Textbook	Barry B. Brey, The Intel Microprocessors: 8086/8088, 80186/80188, 80286, 80386, 80486, Pentium, Pentium Pro Processor, Pentium II, Pentium III, Pentium 4, and Core2 with 64-bit Extensions: Architecture, Programming, and Interfacing, 8th ed., Prentice-Hall, 2008.
References	<ol style="list-style-type: none">1. James Antonakos, An Introduction to the Intel Family of Microprocessors, Prentice-Hall, 3ed Edition, 1999.2. Walter Triebel and Avtar Singh, The 8088 and 8086 Microprocessors: Programming, Interfacing, Software, Hardware, and Applications, 4th ed., Prentice-Hall, 2002.3. Intel 80x86 hardware reference manuals, Intel.
Grading	First Exam 25% Second Exam 25% Two Homework 10% Final Exam 40%
Policies	<ul style="list-style-type: none">• Attendance is required, on time.• No mobile phones.• All submitted work must be yours, and Cheating will not be tolerated.• Homework is due on the 1st and 2nd exams dates.
Tentative Outline	<ul style="list-style-type: none">• Introduction (1.1-1.2)• 8086/8088 Hardware Specifications (9.1-9.6)• Basic I/O Interface (11.1-11.6) <i>First Exam</i>• Memory Interface (10.1-10.7)• Interrupts (12.1-12.4) <i>Second Exam</i>• Direct Memory Access (13.1-13.2)• Bus Interface (15.1-15.6) <i>Final Exam</i>