

Princess Sumaya University of Technology
Computer Engineering Department
22341: Computer Organization
Fall 2010

Instructor	Dr. Gheith Abandah	
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Homepage	http://www.abandah.com/gheith	
Office	EE 448	
Office Hours	Sun 12:00 -1:00, Mon 11:30-12:30, and Thu 10:00-11:00	
Prerequisites	22241: Digital Logic Design	
Time and Room	Section 1: Sun, Tue, and Thu 11:00-12:00	in Room EE 343
	Section 2: Mon and Wed 12:30-2:00	in Room EE 343
Textbook	Patterson and Hennessy. Computer Organization & Design: The Hardware/Software Interface, 4th ed., Morgan Kaufmann, 2009.	
References	<ol style="list-style-type: none">1. Hennessy and Patterson. Computer Architecture: A Quantitative Approach, 4th ed., Morgan Kaufmann, 2007.2. J. Hayes. Computer Architecture and Organization, 3rd ed., McGraw-Hill, 1998.3. M. Mano. Computer System Architecture, 3rd ed., Prentice Hall, 1993.	
Grading	First Exam	25%
	Second Exam	25%
	Two Homeworks	10%
	Final Exam	40%
Policies	<ul style="list-style-type: none">• Attendance is required.• All submitted work must be yours.• Cheating will not be tolerated.• Homeworks are due on the 1st and 2nd exams dates.• This course requires significant effort.	
Tentative Outline	• Introduction and Performance	(1.1-1.4)
	• MIPS Instruction Set	(2.1-2.14)
		<i>First Exam</i>
	• Computer Arithmetic	(3.1-3.10)
	• Datapath and Control Design	(4.1-4.4)
		<i>Second Exam</i>
	• Pipelining	(4.5-4.9)
		<i>Final Exam</i>